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INFORMATION DISCLOSURE STATEMENT BY APPLICANT							APPLICANT: Michael D. Gallant et al.					
							FILING DATE Herewith	GROUP				
U.S. PATENT DOCUMENTS												
Examiner Initial	Document No.			Date	Name			Class	Sub-Class	Filing Date		
	5	6	0	8	6	5	6	3/4/1997	Purcell et al.	364	514 R	
	6	1	0	8	0	3	9	8/22/2000	Linzer et al.	348	398	
FOREIGN PATENT DOCUMENTS												
	Document No.			Date	Country			Class	Sub-Class	Transl.		
										Yes	No	
OTHER DOCUMENTS (Incl. Author, Title, Date, Pertinent Pages, etc.)												
	Sheu-Chich Cheng et al., "A Comparison of Block-Matching Algorithms Mapped to Systolic-Array Implementation", IEEE Transactions on Circuits and Systems for Video Technology, Vol. 7, No. 5, October 1997, pp. 741-757.											
	Eiji Ogura et al., "A 1.2-W Single-Chip MPEG2 MP@ML Video Encoder LSI Including Wide Search Range (H: ± 288 , V: ± 96) Motion Estimation and 81-MPOS Controller", IEEE Journal of Solid-State Circuits, Vol. 33, No. 11, November 1998, pp. 1765-1771.											
Examiner	Date Considered											
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